

Docket No.: PEK-In1178 D

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Jürgen Holz
Div. of Applic. No. : 10/245,627, filed September 17, 2002
Div. filed : April 1, 2004
Title : Method for Fabricating a Semiconductor Structure Using a Protective Layer, and Semiconductor Structure
Examiner : Kevin M. Picardat Group Art Unit: 7435

Hon. Commissioner for Patents
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. 1.98, the following patents and/or publications are cited herewith:

European Patent EP 0 327 412 B1 (Merenda et al.), dated September 21, 1994, and German Translation DE 689 18 301 T2;

Markstein, H.: "Packaging Ideas", Electronic Packaging & Production, February 1992, p. 35.

The above-mentioned references were cited in an *Information Disclosure Statement* dated September 17, 2002, in parent application No. 10/245,627.

U.S. Patent No. 4,654,269 (Lehrer), dated March 31, 1987;

U.S. Patent No. 6,017,614 (Tsai et al.), dated January 25, 2000;

U.S. Patent No. 6,177,338 B1 (Liaw et al.), dated January 23, 2001;

European Patent Application EP 0 312 154 A1 (Gootzen et al.), dated April 19, 1989;

European Patent Application EP 316 550 A2 (Nagy et al.), dated May 24, 1989 ;

Japanese Patent Abstract JP 03 213870 (Yamazaki), dated September 19, 1991;

The above-mentioned references were cited in an *Information Disclosure Statement* dated December 10, 2002, in parent application No. 10/245,627.

U.S. Patent No. 6,265,257 B1 (Hsu et al.), dated July 24, 2001;

U.S. Patent No. 5,472,370 (Malshe et al.), dated December 5, 1995;

U.S. Patent No. 6,063,514 (Nishioka et al.), dated May 16, 2000;

Published Japanese Patent Application JP 02 174269 A (Kamata), dated July 5, 1990.

The above-mentioned references were cited in an Office action dated October 6, 2003, in parent application No. 10/245,627.

Respectfully submitted,

Date: April 1, 2004

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FORM PTO-1449 (SUBSTITUTE)		Attorney Docket No.: Divisional of Applic. No. PEK-In1178 D 10/245,627	
U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		Applicant Jürgen Holz	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))		Filing Date of Divisional April 1, 2004	Group Art Unit 2822

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A	4,654,269	03/87	Lehrer			
	B	6,017,614	01/00	Tsai et al.			
	C	6,177,338 B1	01/01	Liaw et al.			
	D	6,265,257 B1	07/01	Hsu et al.			
	E	5,472,370	12/95	Malshe et al.			
	F	6,063,514	05/00	Nishioka et al.			
	G						
	H						
	I						

FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO
	J	0 312 154 A1	04/89	Europe			
	K	0 316 550 A2	05/89	Europe			
	L	03 213870	09/91	Japan			
	M	689 18 301 T2	08/89	Germany			
	N	0 327 412 B1	09/94	Europe			

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

		Markstein, H.: "Packaging Ideas", Electronic Packaging & Production, February 1992, p. 35.

EXAMINER	DATE CONSIDERED
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

FORM PTO-1449 (SUBSTITUTE)		Attorney Docket No.: Divisional of Applic. No. PEK-In1178 D 10/245,627	
U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		Applicant Jürgen Holz	
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EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A						
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	D						
	E						
	F						
	G						
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	I						

FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO
	J	JP 02 174269 A	07/90	Japan			
	K						
	L						
	M						
	N						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

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